$../main.asm:19:54$, RESET

4031 0400

StopWDT

40B2 5A80 0120

**403F**=0100 0000 0 0 11 1111 opcode=0100(mov) sreg=0000(source register)ad=0(destination addressing mode) b/w=0(word operation) as=11(source addressing mode) dreg=1111(destination register)(R15)

**403F 12E4**

**403E 7C02** similar to above except dreg =R14 so E

**5E0F** opcode=0101(add) sreg=E(R14) ad=0 b/w=0 as=00 dreg=F(R15)

**832A** 1000 0011 0 0 01 1010(uses emulated instruction) opcode 1000-for incd sreg 0011 for R3 constant generator+2 b/w=0(word operation) as=01(source addressing mode,in R3 for 10 subtracts 2) A=destination register

**407F**-0100(opcode for move) 0000(sreg) 0(destination addressing mode register mode) 1(byte operation) as=11(immidiate/-immediate as sreg value not given) dreg=R15 113A(since byte operation the higher bytes will be 11)

**4F9E** (opcode for move, sreg=R15, ad=1()indexed, word operation, as=01(indexed mode),dreg=R14) source index **0002** destination address **0006**

**403B**(opcode move, no sreg, ad=register mode,byte operation,as=immediate mode,dreg=R11 **4444** **4F4B**(move in register mode,byte operationR15 to R11)

**4F0B**(move in register mode,word operationR15 to R11 )

**4292**(opcode=move, sreg=R2,ad=1,word operation,as=01(absolute mode),dreg=R2(absolute mode))**2222** **3333**

**4CAE**(opcode=move sreg=R12,ad=1,word operation,as=01(indirect register mode),dreg=R14(index mode)) **0000**

**4E0A**(opcode-move,sreg=R14,ad=0,word operation,as=00(index),dreg=R11(register mode))

**4E0B**(opcode=move,seg=R14,ad=0,word operation,as=00,dreg=R10)

**4ABB**(opcode=move,sreg=R11,ad=1,word operation,as=11(auto increment),dreg=R10(indexed mode)) **0000**

**9B0A**(opcode=1001(compare) sreg=R11, ad=0,word operation,ad=00,dreg=R10(register mode for both))

**903E**(opcode=1001(compare) sreg=0, ad=0,word operation,as=11(immediate mode),dreg=R10(register mode)) **6789**

**FA0B** (opcode=1111(logical and),sreg=R10,ad=0,word operation,as=00(register mode),dreg=R11(register mode))

**100F** (opcode=00010000(through carry),ad=0,word operation,as=00,dreg=R15(register mode))

**110F** (opcode=00010001(rotate right arithematically),ad=0,word operation,as=00,dreg=R15(register mode))

**120F**(opcode=0001000110000 dreg=R15)

**413E** (emulated instruction pop opcode-0100(move),sreg=R1(+2),ad=0,word,as=11,dreg=R14)

